MANDATORY DISCLOSURE IN AICTE FORM-16
SRIKRISHNA ENGINEERING COLLEGE
(Approved by AICTE New Delhi & affiliated to Anna University, Chennai-600005)
PANAPAKKAM, SERPANANCHERRY(PO), NEAR PADAPPAI
(Via) TAMBARAM- CHENNAI- 601 301

MANDATORY DISCLOSURE BY THE MANAGEMENT OF SKEC

Statuary Notice by AICTE, New Delhi

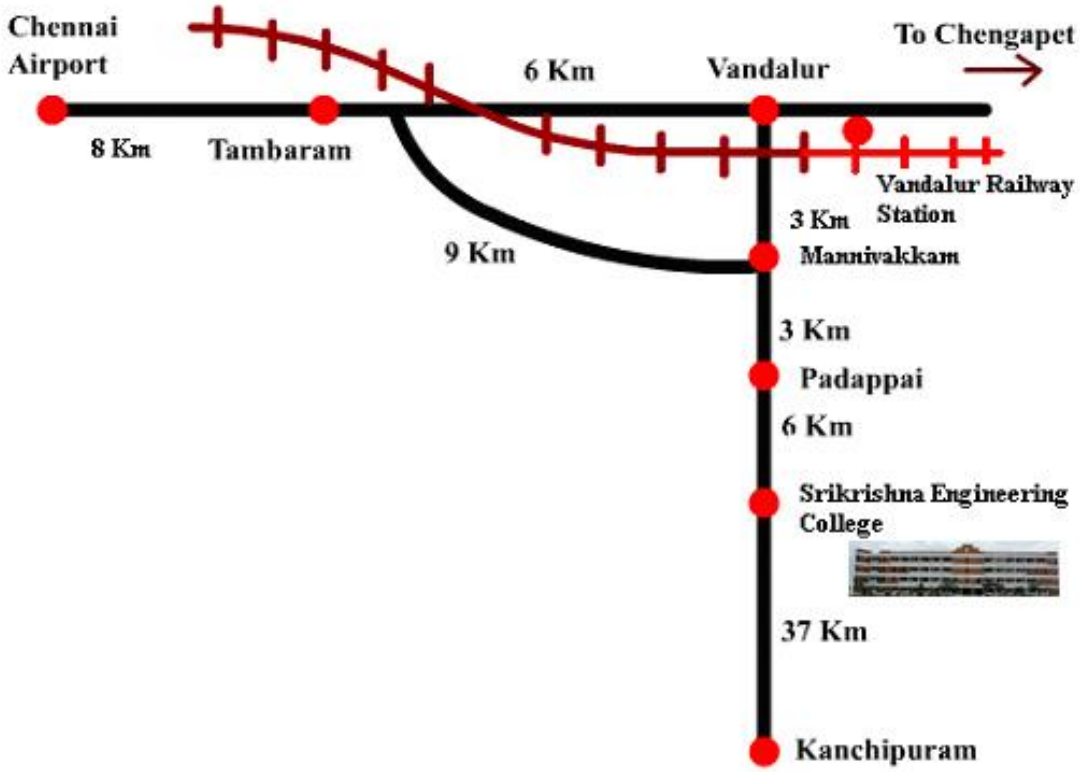
THE FOLLOWING INFORMATION IS TO BE GIVEN IN THE INFORMATION BROCHURE BESIDES BEING HOSTED ON THE INSTITUTION'S OFFICIAL WEBSITE.

"The information has been provided by the concerned institution and the onus of authenticity lies with the institution and not on AICTE."

I. NAME OF THE INSTITUTION

Name	**SRI KRISHNA ENGINEERING COLLEGE**	
Address	Panapakkam, Serpanancherry(PO) (Near) Padappai, (via) Tambaram, Chennai – 601301	
Taluk	Kundrathur	The College is functioning in the permanent location approved by AICTE
District	Kancheepuram	
Pin code	601 301	
State	Tamil Nadu	
STD Code	044	
Phone	8110861000, 8110862000 044-22761313	srikrishnacollege@sify.com
FAX	-----	
Website	www.srikrishnacollege.net	Email: srikrishnacollege@sify.com
Nearest Railway Stations	Tambaram Vandalur	Distance 18 Km Distance 12 Km
Nearest Airport	Chennai Airport	Distance 26 Km

LOCATION MAP



II. NAME & ADDRESS OF THE PRINCIPAL

Name of the Principal	**Dr. G. RAVIKUMAR**		
Qualification	M.E.,Ph.D.,	Date of Birth	06-04-1982
STD Code	044	Phone No.(O): 8110861000	Fax No. 22263223
STD Code	044	Phone No.(R): 9710119126	Fax No.
Email:	Principal.skec@gmail.com		

III. NAME OF THE AFFILIATING UNIVERSITY

Name	**Anna University Chennai**		
Address	The Registrar, Anna University Chennai Guindy, Chennai-600025, Tamilnadu. www.annauniv.edu		
Pin Code	600025	Period of Affiliation	2021- 2022
STD Code	044	Phone No.	22352161
Fax No.	22352161	E-mail:	registrar@annauniv.edu

IV. GOVERNANCE

Members of the Board and their brief background: Sri Krishna Engineering College is one of the Institutions sponsored by the **Sri Krishna Educational Trust** with the ultimate objective of imparting technical Education through advanced level Science and Technological innovations. **The Founder Chairman of the College is Dr.R.Vivekanandhan** who hails from a respectable and humble family with excellent educational background. The College is a self-financing Institution under the above Trust and it is governed by a duly constituted Board of Governors. The College is approved by AICTE, New Delhi and duly affiliated to Anna University.

The college campus spreads in vast area of **30 acres** adjoining the state highway leading to Kancheepuram on the west and Chennai Via Tambaram on the east.

The Board of Governors constituting the welfare and administration of College:

S. No	Names of the Members	Designation	Qualification	Address
1	Dr.R.Vivekanandhan	Chairman of the Trust	Master of Education	2 Rajaji Lane West Tambaram, Chennai-600045
2	Mrs.R.Rajeswari	Secretary	SSLC	2 Rajaji Lane West Tambaram, Chennai-600045
3	Mr.R.Arulanandhan	Trustee	M A, IDAS	2 Rajaji Lane West Tambaram, Chennai-600045
4	Mrs.N.Vanitha	Trustee	MCA	2 Rajaji Lane West Tambaram, Chennai-600045
6	Dr.G.RAVIKUMAR	Principal & Member Secretary	Ph D	Sri Krishna Engineering College Panapakkam Chennai -601 301

Members of Academic Advisory Body

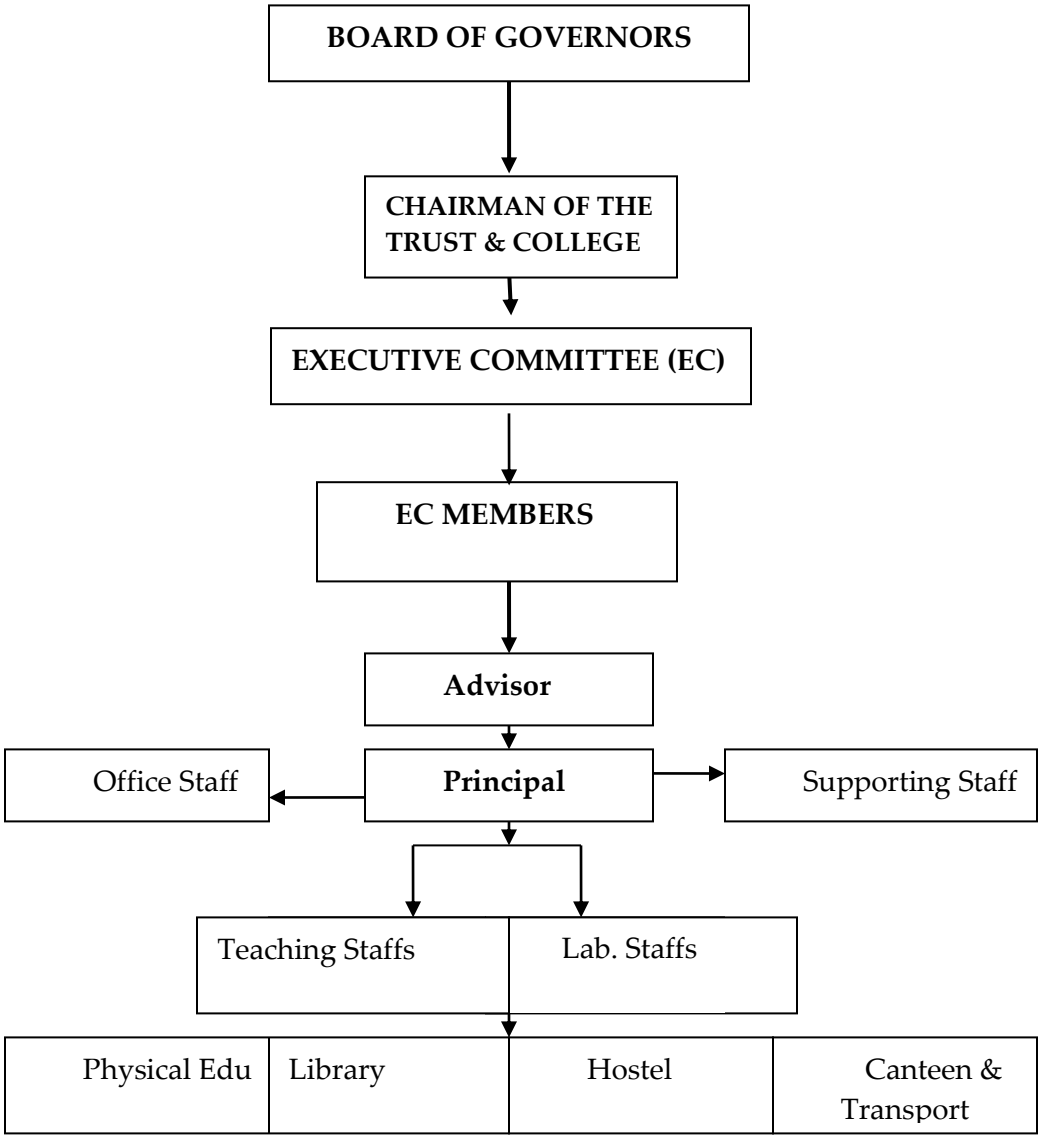
- Dr.R.Vivekanandhan, Chairman of the Trust and the College
- Mr.R.Arulanandhan, Indian Defense Administrative Service, New Delhi
- Dr.G.RAVIKUMAR. Ph. D . Principal, SKEC
- Prof.N.SIVANESAN . Vice-Principal, SKEC

Frequency of the Board Meetings and Academic Advisory Body.

- Board Meetings are held normally twice in a year but should there be a need and urgency meetings are held at short notice.

- Academic Advisory Meetings are held twice in **a semester i.e., at the** Beginning of the Semester and at the end**.**

ORGANIZATIONAL CHART AND PROCESSES



Nature and Extent of involvement of faculty and students in academic affairs/improvements.

The academic affairs of the College are the important segments for the overall academic performance of the students. The teaching learning process plays a vital role in this orientation. The teachers follow not only the rudiments of teaching but they get involved in all facets of teaching processes. At the beginning of the semester itself these academic aspects are planned for the successful running of the academic programme.

- **Mechanism/Norms & Procedure for democratic/good Governance**

  The norms and procedure for the Governance of the College are decided by the top management after getting feedback from supporting technical and administrative personnel. From the experiences gained, for good governance, these are periodically reviewed and modified.

❖ **Students Feedback on Institutional Governance/faculty performance**

Students' feedback is very important in running academic programme as well as the development of the College. The Management activities and the academic performance of the teachers are under scanner by the students' feedback. This is the vital segment for the real-time improvements in both academic and developmental activities. Our College is more transparent in this segment and in a semester feedbacks are obtained at appropriate intervals.

❖ **Grievance redressed mechanism for faculty, staff and students**

The performance of the faculty, other staffs and students is the 'Gauge Index'. This performance will go down if the genuine grievances of the above personnel are unattended to. Sri Krishna Engineering College top Management is very much committed and seized this point. The faculty, staffs and students are freely asked to interact with Principal and all Office bearers. All the legitimate grievances are being redressed to the advantage of these people.

V. PROGRAMMES

A Name of the Programmes approved by the AICTE

UG Programmes

1. B.E (Electronics & Communication Engineering.)	(60)
2. B.E (Computer Science & Engineering.)	(60)
3. B.E (Electrical & Electronics Engineering.)	(30)
4. B.Tech. (Information Technology)	(60)
5. 5. B.Tech. (Fashion Technology)	(60)

PG Programmes

1. M.E (Computer Science and Engineering)	(18)
2. M.E (Applied Electronics)	(18)
3. MBA (Master of Business Administration)	(60)

B Name of the programmes for approval by AICTE

Name of the Programmes accredited by the AICTE

∗ Now we have become eligible for accreditation as five batches have passed out.

Application for NBA is being worked out and shortly the same will be sent to AICTE, New Delhi.

Details for the Approved Programmes:

Branch Name: Bachelor of Engineering (Electronics & Communication Engineering.)

No. of seats	*Course Duration*	*Cut of marks/ranks*	*Fees(Rs.) Per year*	*Placement Facilities*	*Students Placed*	*Minimum Salary*	*Maximum Salary*
60	*4 years*	*248.00*	*Rs.32, 500*	*Yes available*	*12*	*20,000/m*	*45,000/m*

Average Salary of all those placed= Rs.25000/m

Number of faculty employed and left during the last three years: 3

Branch Name: Bachelor of Engineering (Computer Science & Engineering.)

No. of seats	*Course Duration*	*Cut of marks/ranks*	*Fees(Rs.) per year*	*Placement Facilities*	*Students Placed*	*Minimum Salary*	*Maximum Salary*
60	*4 years*	*250.00*	*Rs.32,500 per year*	*Yes available*	*18*	*21,000/m*	*42,000/m*

Average Salary of all those placed= Rs.26500/m

Number of faculty employed and left during the last three years: *5*

Branch Name: Bachelor of Engineering (Electrical & Electronics Engineering.)

No. of seats	*Course Duration*	*Cut of marks/ranks*	*Fees(Rs.) Per year*	*Placement Facilities*	*Students Placed*	*Minimum Salary*	*Maximum Salary*
60	*4 years*	*216.00*	*Rs.50000 per year*	*Yes available*	*12*	*The first Batch has not gone out yet*	

Number of faculty employed and left during the last three years: 0

Branch Name: Bachelor of Technology (Information Technology.)

No. of seats	*Course Duration*	*Cut of marks/ranks*	*Fees(Rs.) Per year*	*Placement Facilities*	*Students Placed*	*Minimum Salary*	*Maximum Salary*
60	*4 years*	*246.00*	*Rs.32500 per year*	*Yes available*	*12*	*18,000/m*	*40,000/m*

Average Salary of all those placed= Rs.26500/m

Number of faculty employed and left during the last three years: *5*

Branch Name: Bachelor of Technology (Fashion Technology.)

No. of seats	*Course Duration*	*Cut of marks/ranks*	*Fees(Rs.) Per year*	*Placement Facilities*	*Students Placed*	*Minimum Salary*	*Maximum Salary*
60	*4 years*		*Rs.32500 per year*	*Yes available*	*12*	*18,000/m*	*43,000/m*

Average Salary of all those placed= -
Number of faculty employed and left during the last three years: 0

Branch Name: Master of Engineering (Computer Science and Engineering.)

No. of seats	*Course Duration*	*Cut of marks/ranks*	*Fees(Rs.) Per year*	*Placement Facilities*	*Students Placed*	*Minimum Salary*	*Maximum Salary*
18	*2 years*	*246.00*	*Rs.32500 per year*	*Yes available*			

Average Salary of all those placed= -
Number of faculty employed and left during the last three years: 1

Branch Name: Master of Engineering (Applied Electronics.)

No. of seats	*Course Duration*	*Cut of marks/ranks*	*Fees(Rs.) Per year*	*Placement Facilities*	*Students Placed*	*Minimum Salary*	*Maximum Salary*
18	*2 years*	*246.00*	*Rs.32500 per year*	*Yes available*			

Average Salary of all those placed= -
Number of faculty employed and left during the last three years: 0

Name and duration of programme(s) having affiliation/collaboration with Foreign University(s)/Institution(s) and being run in the same Campus along with status of their AICTE approval.

No programme is conducted having affiliation//collaboration with any of the Foreign Universities/Institutions

VI. FACULTY

Branch wise list faculty members:

Sl No	Branch	Permanent Faculty	Visiting Faculty	Adjunct Faculty	Guest faculty	Permanent faculty: Student ratio
1	ECE	11	Nil	Nil	1	1:15
2	CSE	11	Nil	Nil	1	
3	IT	9	Nil	Nil	1	
4	EEE	8	Nil	Nil	Nil	
5	FT	9			1	
6	First Year	14	Nil	Nil	Nil	
7	M.E - CSE	2	Nil			
8	M.E – AE	2				
9	MBA	6				

The staff- student ratio will be 1:13.84 If the adjunct and guest lecturers are also included with respect to the actual admission in the last 3 years then it will be around 1:12.41

Advertisements are given frequently for senior faculty positions and as and when suitable candidates are forthcoming, they are being immediately appointed.

VII. PROFILE OF PRINCIPAL WITH QUALIFICATIONS, TOTAL EXPERIENCE, AGE AND DURATION OF EMPLOYMENT AT THE INSTITUTE CONCERNED.

NAME: Dr. G.RAVIKUMAR
DATE OF BIRTH: 06.04.1982
Educational Qualification: B.Tech., M.Tech., Ph.D.,

Work Experience:

(Teaching)

- Started the career as Lecturer at Sri Krishna Engineering College (01.07.2004-30.04.2008)
- Worked as Lecturer in PRRM College of Engineering (27.07.2010- 06.10.2011)
- Worked as Assistant Professor in Holy Mary Institute of Technology (06.06.2011 – 07.06.2013)
- Avanthi Institute of Engineering and Technology (27.06.2013 – 10.10.2017)

- Worked as Associate Professor in Sri Krishna Engineering College (03.12.2019 – 04.03.2022)
- Joined as Principal in Sri Krishna Engineering College from 07.03.2022

(Industry and Research)

Area of specialization :

Faculty of Electronics and Communication Engineering

Subject Teaching : Electric Circuit

UG Electrical Machines

Advanced Electrical Machines

Electronic Devices & Circuit

Electronics Instrumentations

Electrical Instrumentations & Control

Utilization of Electrical Power

Virtual Instruments

Power Electronics

Electric Devices

Advanced Electrical Instruments

PG : Advanced Logic Design (AE)

Power Plant Engineering (PS)

Research Guide : No.of Papers Published

Masters : National Journals

3 national

2 International Journals

15 Conferences

Project carried out : Guided UG/PG student Projects

Technology Transfer : Modified Automobile Alternator (AE 115) for Windmill applications (LUCAS TVS Chennai)

Research publications : Papers on CATES

No of Books Published with details

1. Instrumentations- UG Students (Allied Publishers)
2. Electronics - Polytechnic Students (DTE,Madras)
3. Electro Technology - For marine Engineers (MASSA)

(Foreign Assignments)

- Visited AL GHURAIR University Dubai to set up their advanced Electrical Machines Laboratory
- Prepared Laboratory instruction material using virtual Lab concepts, 2016
- Visited Sharja Institute of Higher Learning Sharaja, Dubai and delivered technical lectures, 2017

(Professional Societies)

- Active number of many leading instates and societies like IEEE, ISA, IETE, IE, NIQR besides council member in some of them

(Present Field of Activities)

- Computer aided teaching study planning, job oriented classes, ergonomics in teaching, learning and evaluation -Design develop test implement paper less examination system in technical university of higher learning (Virtual Learning)

(Personnel Details)

Date of birth and Age: 06.04.1982, Age 40
Address: No.2/32. Bazaar Street
Padappai -601301
Kanchipuram District
Phone: (Res) -----
(Cell) 9710119126

Existing faculty for Sri Krishna Engineering College,Panapakkam,Chennai - 601 301

Department of Computer Science and Engineering

Name of the course	S.No	Name(s) of the Teaching Faculty	Designation (Lect/Asst.Prof	Qualification with field of Specialization with class/division of passing		
				UG	PG	Doct
	1	N.Sivanesan	Associate Professor	B.E	M.E	(Ph.D)
UG -	2	P.Vijayan	Asst.Prof	B.E	M.E	
B.E	3	A.Bharath kumar	Asst.Prof	B.E	M.E	
CSE	4	M.Abila	Asst.Prof	B.E	M.E	
	5	M.Jaganathan	Asst.Prof	B,E	M.E	
	6	G.Manimegalai	Asst.Prof	B.E	M.E	
	7	V.N.S.L.L.S.R.Murthy	Asst.Prof	B.E	M.E	
	8	R.R.Jayashree	Asst.Prof	B.E	M.E	

	9	R.Parthiban	Asst.Prof	B.E	M.E	
	11	G.Hariprasad	Asst.Prof	B.E	M.E	
	12	S.V.Elumalai	Asst.Prof	B.E	M.E	

Faculty for Sri Krishna Engineering College,Panapakkam, Chennai - 601 301

Department of Information Technology

Name of the course	S.No	Name(s) of the Teaching Faculty	Designation (Lect/Asst.Prof	Qualification with field of Specialization with class/division of passing		
				UG	PG	Doct
	1	Dr.E.Seshatheri	Associate Professor	B.Tech	M.E	Ph.D
	2	S.Balamurugan	Asst.Prof	M.Sc	M.E	
UG	3	K.Natarajan	Asst.Prof	B.Tech	M.E	
Level	4	R.Udhaya mehala	Asst.Prof	B.Tech	M.E	
	5	P.Indhumathi	Asst.Prof	B.Tech	M.E	
B.Tech	6	V.J.Muthulakshmi	Asst.Prof	B.Tech	M.E	
	7	M.Manju	Asst.Prof	B.Tech	M.E	
	8	Antony Bagya shree	Asst.Prof	B.E	M.E	

	9	M.Shyni	Asst.Prof	B.Tech	M.E	
	10	U.Jagadeesan	Asst.Prof	B.Tech	M.E	

Sri Krishna Engineering College,Panapakkam,Chennai - 601 301

Department of Electronics and Communication Engineering

Name of the course	S.No	Name(s) of the Teaching Faculty	Designation (Lect/Asst.Prof	Qualification with field of Specialization with class/division of passing		
				UG	PG	Doct
	1	S.VIJAY	Associate Professor	B.E	M.E	
	2	S.Karthick	Asst.Prof	B.E	M.E	
UG	3	Anto Sagaya Pricilaa	Asst.Prof	B.E	M.E	
Level	4	R.Nivetha	Asst.Prof	B.E	M.E	
	5	M.Rajasekar	Asst.Prof	B.E	M.E	
B.E	6	Durgesh Kannan	Asst.Prof	B.E	M.E	
	7	M.Tamilarasan	Asst.Prof	B.E	M.E	
	8	R.Sathya	Asst.Prof	B.E	M.E	

ECE	9	C.Saranya	Asst.Prof	B.E	M.E	
	10	S.Saraswathi	Asst.Prof	B.E	M.E	
	11	Prithviraj	Asst.Prof	B.E	M.E	

Existing faculty for Sri Krishna Engineering College,Panapakkam,Chennai – 601 301

Department of Electrical and Electronics Engineering

Name of the course	S.No	Name(s) of the Teaching Faculty	Designation (Lect/Asst.Prof	Qualification with field of Specialization with class/division of passing		
				UG	PG	Doct
UG Level B.E-EEE	1	Dr.G.Ravikumar	Professor	B.E	M.E	Ph.D
	2	K.S.Jeevanandhan	Asst Prof	B.E	M.E	
	3	N.Hariprakash	Asst Prof	B.E	M.E	
	4	R.Padmavathi	Lecturer	B.E	M.E	
	5	P.Thiruselvi	Lecturer	B.E	M.E	
	6	D.Jayashree	Lecturer	B.E	M.E	
	7	R.Asha	Lecturer	B.E	M.E	
	8	A.Geetha	Lecturer	B.E	M.E	

Existing faculty for Sri Krishna Engineering College,Panapakkam,Chennai - 601 301

Science and Humanities

Name of the course	S.No	Name(s) of the Teaching Faculty	Designation (Lect/Asst.Prof	Qualification with field of Specialization with class/division of passing		
				UG	PG	Doct
UG Level **S &H**	1	N.Durairaj	Professor		M.Sc maths	M.Phil
	2	K.Kalpana Rani	Asst.Prof		M.Sc maths	M.Phil
	3	K.Vimala Rani	Asst.Prof		M.Sc Phy	M.Phil Phy
	4	G.Manimegalai	Asst.Prof		M.Sc Chem	M.Phil Chem
	5	Juliet Pushpam	Asst.Prof		M.Sc Phy	M.Phil
	6	R.Sriram	Asst.Prof		M.Sc CS	M.Phil
	7	N.Mohan Kumar	Asst.Prof		M.Sc Chem	M.Phil
	8	E.D.Anbumozhi	Asst.Prof		M.Sc Chem	M.Phil Chem
	9	G.Bharathy	Asst.Prof		M.A Eng	M.Phil
	10	S.Bharathi	Asst.Prof		M.A	M.Phil

					Eng	
	11	V.Poovarasan	Asst.Prof		M.A Eng	M.Phil
	12	G.Poornima	Asst.Prof		M.Sc Maths	M.Phil Maths
	13	B.Kavitha	Asst.Prof		M.Sc Phy	M.Phil Phy
	14	Elaya pallavan	Asst.Prof		B.E Mech	M.E

Discipline wise existing faculty for Sri Krishna Engineering College,Panapakkam,Chennai - 601 301

M.E - Computer Science and Engineering

Name of the course	S.No	Name(s) of the Teaching Faculty	Designation (Lect/Asst.Prof	Qualification with field of Specialization with class/division of passing		
				UG	PG	Doct
PG Level **M.E - CSE**	1	S.N.Sheel Evanjelin Prasad	Associate Professor	MCA	M.E	
	2	N.Vanitha	Associate Professor	B.E	M.E	
	3	S.Srividhya	Asst.Prof	B.E	M.E	

Faculty appointed for the proposed additional course in M.E Applied Electronics in

Name of the course	S.No	Name(s) of the Teaching Faculty	Designation (Lect/Asst.Prof	Qualification with field of Specialization with

				class/division of passing		
				UG	PG	Doct
PG Level **M.E -** Applied	1	Rekha Sharmily	Asst. Prof	B.E	M.E	
	2	O.Chitraprabha	Asst. Prof	B.E	M.E	

Faculty appointed for the proposed MBA course in Sri Krishna Engineering College,Panapakkam,Chennai - 601 301

MBA

Name of the course	S.No	Name(s) of the Teaching Faculty	Designation (Lect/Asst.Prof	Qualification with field of Specialization with class/division of passing		
				UG	PG	Doct
MBA	1	Dr.R.Mohan Kumar	Professor	B.Com	M.BA	Ph.D
	2	Dr.Viswambaradevi	Asst.Prof	BDS	M.BA	Ph.D
	3.	A.Sathyavaradhan	Asst.Prof	B.E	MBA	
	4	R.Samuel	Asst.Prof	BBA	M.BA	
	5	S.Kalaivani	Asst.Prof	BBA	M.BA	
	6	N.Neeranjani	Asst.Prof	B.Com	MBA	

VIII-FEE

Details of fee, fixed by the Institution for 2008- 2009

Management Seat as fixed by Govt of Tamil Nadu

Counseling Seat as per Govt Norms

Time schedule for payment of fee for the entire programme:

To be paid fully during the beginning of the academic year

No. of Fee waivers granted with amount and name of students (Rs.37500) each.

Deepthi	-	III CSE	R.Prabhakaran	-	IV ECE
S.Prem Kumar	-	IV ECE	N.Haripriya	-	IVECE
Kayalvizhi	-	IV ECE	Rabick Raja	-	IV ECE
Venkatesan	-	IV ECE	S.Yugaselvi	-	IV ECE
D.Bharathi	-	IV CSE	R.Bhavani	-	IV CSE
M.R.Divya	-	IV CSE	Ezhilarasi	-	IV CSE
K.Karithikeyan	-	IV CSE	G.Kavitha	-	IV CSE
Mahesh.J	-	IV CSE	K.Manivannan	-	IV CSE
L.Rajasekar	-	IV CSE	Sarala.E	-	IV CSE
R.Sasidevi	-	IV CSE	P.Susila	-	IV CSE
Umamaheshwari	-	IV CSE	K.Veena	-	IV CSE

Number of scholarship offered by the Institute, duration and amount:

Cash gifts are given to the class toppers and subject toppers every semester on their University performance

Criteria for fee waivers/scholarship: + 2 marks

Eligibility for fee waivers*: Mainly Merit cum marks*

Eligibility for Scholarship*: Students who have more than 90% of marks in Plus 2 examinations are given fee concession by the Management.*

LAPTOP Computers: The Engineering students who consistently score high in of all semesters are being given LAPTOP Computers free of cost and those who secure anything between 80 and 89% are being given cash awards

Estimated cost of boarding and Lodging in Hostels. *Rs. 35,0000 for full year(Rent/boarding/electricity included)*

VIII. ADMISSION

Number of seats sanctioned with the year of approval.

2000-01 : 60 x 3 = 180	2001-02 : 60 x 3 = 180
2003-04 : 60 x 3 = 180	2004-05 : 60 x 3 = 180
2005-06 : 60 x 4 = 240	2006-07 : 60 x 4 = 240

2007 – 08 : = 348 Excluding Mech which was already surrendered including 18 seats in M.E Computer Science and Engineering	
2007-08 : 60 x 5 = 300 M.E- CSE - 18	2008-09 60 x 5 = 300 M.E- Applied Electronics – 18 MBA - 60
2009 – 10 : 60 x 3 =180 120 x 2=240 PG – 2 x 18 = 36 1x 60 = 60	2010 – 11 : 60 x 2 =180 120 x 2=240 90 x 1 = 90 PG – 2 x 18 = 36 1x 60 = 60
2011 – 12 : 60 x 2 =180 120 x 2=240 90 x 1 = 90 PG – 2 x 18 = 36 1x 60 = 60	2012 – 13 : 60 x 2 =180 120 x 2=240 90 x 1 = 90 PG – 2 x 18 = 36 1x 60 = 60
2013– 14 : 60 x 2 =180 120 x 2=240 90 x 1 = 90 PG – 2 x 18 = 36 1x 60 = 60	2014 – 15 : 60 x 2 =180 120 x 2=240 90 x 1 = 90 PG – 2 x 18 = 36 1x 60 = 60
2015 – 16 : 60 x 2 =180 90 x 3= 270 PG – 2 x 18 = 36 1x 60 = 60	2016 – 17 : 60 x 2 =180 90 x 3= 270 PG – 2 x 18 = 36 1x 60 = 60
2017 – 18 : 60 x 2 =180 90 x 3= 270 PG – 2 x 18 = 36 1x 60 = 60	2018 – 19 : 60 x 2 =180 90 x 3= 270 PG – 2 x 18 = 36 1x 60 = 60
2019 – 20 : 60 x 5 =300 PG – 2 x 18 = 36 1x 60 = 60	2020 – 21 : 60 x 4 =240 30 x1 = 30 PG – 2 x 18 = 36 1x 60 = 60
2021- 22 UG Programmes B,E – CSE,IT,ECE ,FT 60 x 4 = 240 B.E - EEE: 30 x 1 = 30	PG Programmes M.E CSE - 18 M.E Applied Electornics -18 MBA - 60

Number of students admitted under various categories each year in the last three years for the Sanctioned courses.

Year	Name of Course	No. of students admitted under various categories (ST, SC, BC OBC & Others)					
		ST	SC	BC	MBC	Others	Total
2008-2009	BE-Electronics & Communication Engineering		12	26	32	10	80
2007-2008	BE-Electronics & Communication Engineering		1	29	10	19	59
2006-2007	--do--		1	29	10	10	50
2008-2009	BE-Computer Science &Engineering		12	36	32	20	100
2007-2008	BE-Computer Science &Engineering		10	26	11	27	84
2006-2007	--do--		10	26	11	13	60
2008-2009	B.Tech-Information Technology		12	41	37	20	110
2007-2008	B Tech-Information Technology		7	26	14	12	59
2006-2007	--do--		7	26	14	11	58
2008-2009	Electrical & Electronics Engineering		7	10	20	18	55
2007-2008	Electrical & Electronics Engineering		3	5	10	18	33
2006-2007	--do--			1	1	1	3
2008 -09	Fashion technology		2	5	8	5	20
2008-09	M.E - CSE		3	5	8	2	18
2007-08	M.E - CSE		1	3	3	2	9
2008-09	M.E–Applied Electronics		1	4	3	2	10
2008-09	MBA		5	20	7	3	35

Number of applications received during last two years for admission under Management Quota and number admitted.

	ECE		CSE		IT		EEE	
Year	R	A	R	A	R	A	R	A
2020-21	35	10	35	10	40	8	30	4
2021-22	45	8	23	6	35	9	35	2
	FT		M.E CSE		**M.E Appl.El**		**MBA**	
Year	R	A	R	A	**R**	A	R	A
2020-21	30	18	25	2	**25**	5	120	35
2021-22	25	18	23	18	**18**	4	130	60

R: Received and A: Admitted

IX. ADMISSION PROCEDURE

Mention the admission test being followed, name and address of the Test Agency and its URL (website).

Govt Admission: Entrance examination for the admission to the professional degree course, (TNPCEE, conducted by the Anna University, Chennai-25): Web site: www.annauniv.edu

Management Admission:

This is being done hitherto by admitting the students who wrote the CET conducted by Consortium of Tamilnadu professional Colleges

Calendar for admission against management/vacant seats:

Last date for request for applications.	15-06-2022
Last date for submission of application.	07-07-2022
Dates for announcing final results.	21-07-2022
Release of admission list (main list and waiting list should be announced on the same day)	01-08-2022
Date for acceptance by the candidate (time given should in no case be less than 15 days)	14-07-2022
Last date for closing of admission.	28-07-2022
Starting of the Academic session.	01-08-2022
The waiting list should be activated only on the expiry of date of main list.	Yes it will be activated as per Govt.order
The policy of refund of the fee, in case of withdrawal, should be clearly notified.	Notified Strictly as per Govt.Order

XI. CRITERIA AND WEIGHTAGES FOR ADMISSION:

Describe each criteria with its respective weightages i.e. Admission Test, marks in qualifying examination etc.

∗ *Rank in the Common entrance Examination should meet the minimum mark requirement specified by the Govt. of Tamilnadu.*

Mention the minimum level of acceptance, if any.

∗ Criteria as fixed by Government of Tamil Nadu

Mention the cut-off levels of percentage & percentile scores of the candidates in the admission test for the last three years.

∗This is as follows:

Year	Cut of Levels of %	Percentile scores
2019	148.00	NA
2020	149.00	NA
2021	148.00	NA

Item No I - XI must be given in information brochure and must be hosted as fixed content in the website of the Institution. The Website must be dynamically updated with regard to XII–XV.

XII. APPLICATION FORM: Downloadable application form, with online submission possibilities

XIII. LIST OF APPLICANTS:

List of candidates whose applications have been received along with percentile/percentage score for each of the qualifying examination in separate categories for open seats. List of candidates who have applied along with percentage and percentile score for Management quota seat.

* For the Management and Government quota separate list are being prepared by the examining bodies and displayed widely.

XIV. RESULTS OF ADMISSION UNDER MANAGEMENT SEATS/VACANT SEATS

Composition of selection team for admission under Management Quota with the brief profiles of members (This information be made available in the public domain after the admission process is over)

No committee constituted. Admission is as per the rank obtained in CET* Score of the individual candidates admitted arranged in order of merit. *No separate rank / score

List of candidates who have been offered admission.

This is as furnished in Item XI above

Waiting list of the candidates in order of merit to be operative from the last date of joining of the first list candidates.

**No waiting list*

List of the candidates who joined within the date, vacancy position in each category before operation of waiting list.

* *Excepting for a course like EEE, for all other courses 100% admission was there and that there was no waiting of candidates*

XV. INFORMATION ON INFRASTRUCTURE AND OTHER RESOURCES AVAILABLE

LIBRARY: Number of Library books/Titles/Journals available

Sl. No	*Course(s)*	*Number of titles of the books*	*Number of volumes*	*Journals*	
				National	*International*
1	*ECE*	650	5000	6	6
2	*CSE*	620	6000	6	6
3	*IT*	480	5750	6	6
4	*EEE*	330	4100	6	6
5	*FT*	310	1250	6	6
6	*First Year*	510	2500	6	6
7	*M.E- CSE*	150	1250	6	6
8	*M.E- AE*	150	1250	6	6
9	*MBA*	150	1450	6	6
	Total	3430	27050	54	54

LIBRARY JOURNALS LIST

S.NO	NATIONAL JOURNALS	INTERNATIONAL JOURNALS
1	Indian Journal of Radio and Space Physics	Engineering Journal
2	Colourage	Electronic Education
3	IEEMA	Wireless System Design
4	Industrial Automation	Elector Electronics
5	Journal of Engineering Technology	Practical Wireless
6	Network Computing	Electronics World
7	Database System	ASCE News
8	Computer Graphics	Software Engineering
9		Computer Architecture
10		Operating System

List of online National/International Journals subscribed:

The library is being modernized to have exclusive online journal facilities. This will take another 6 months time

E-Library facilities:

165 Electronic CDs comprising Library accession of Books are available

LABORATORY FACILITIES

LIST OF MAJOR EQUIPEMENT / FACILITIES

Department: CSE & I T

Central computing facilities

S.NO	DESCRIPTION OF ITEMS	QUANTITY	RATE (Rs.) /Unit	TOTAL COST(Rs.)
		Major Items		
1	SERVER	2	55,000/-	55,000/-
2	CLIENT	351	22,000/-	64,90,000/-
3	PRINTER	35	12,000/-	3,00,000/-
4	UPS	35 KVA	4,30,000/-	4,30,000 /-
5	Windows 2003 server	5 Users	55,150/-	2,75,750 /-
6	Windows XP Professional	250 Users	8,500/- + 249 L x 3000	7,55,000 /-
7	Linux Red hat	60 Users	8,000/-	4,80,000 /-
8	SQL Server 2000	30 Users	67,000/- + 29 L x 3000	1,54,000 /-
9	ISA Server	1 Users	45,000/-	45,000/-
10	Exchange Sever	1 Users	28,000/-	28,000/-
11	Windows Office XP	10 Users	13,000/-	1,30,000 /-
12	Visual Studio Professional	1 (Noof users50)	68,500/- + 49 License	68,500/-
13	Borland C++	1	4,800/-	4,800/-
14	Outlook Express 2003	1	6,000/-	6,000/-
15	Front page 2003	1	8,000/-	8,000/-
16	MS Visual Studio.Net 2003 AE Full Pack	1	3,900/-	3,900/-
17	MS Office 2003 Professional AE Full Pack	1	7,800/-	7,800/-
18	Studio MX 2004 AE Full Pack	1	8,600/-	8,600/-
19	Developer 2000	1	8,000/-	8,000/-
20	JDK 1.3	1	Freeware	Freeware
TOTAL				93,08,350/-

(CSE 01)NAME OF THE LABORATORY:CS1205OBJECT ORIENTED PROGRAMMING LAB

LIST OF EXPERIMENTAL SETUP –ODD SEMESTER FOR THE ABOVE LAB.

S.NO	NAME OF THE EXPERIMENTS
1	Programs Using Functions
2	Simple Classes For Understanding Objects, Member Functions And Constructors
3	Compile Time Polymorphism
4	Runtime Polymorphism
5	File Handling
6	Simple Java Applications
7	Simple Package Creation
8	Interfaces
9	Threading
10	Exception Handling Mechanism In Java

(CSE 02) NAME OF THE LABORATORY: CS 1404 INTERNET PROGRAMMING LAB
LIST OF EXPERIMENTAL SETUP –ODD SEMESTER FOR THE ABOVE LAB

S.No:	**Name of the Experiments**
1	Write programs in Java to demonstrate the use of following components Text fields, buttons, Scrollbar, Choice, List and Check box
2	Write Java programs to demonstrate the use of various Layouts like Flow Layout, Border Layout, Grid layout, Grid bag layout and card layout
3	Write programs in Java to create applets incorporating the following features:Create a color palette with matrix of buttons
4	Write programs in Java to do the following. Set the URL of another server. Download the homepage of the server.
5	Write programs in Java using sockets to implement the following: HTTP request FTP SMTP POP3
6	Write a program in Java for creating simple chat application With datagram sockets and datagram packets.
7	Write programs in Java using Servlets:To invoke servlets from HTML formsTo invoke servlets from Applets
8	Write programs in Java to create three-tier applications using servlets For conducting on-line examination.For displaying student mark list. Assume that student information is available in a database which has been stored in a database server.
9	Create a web page with the following using HTMLTo embed a map in a web pageTo fix the hot spots in that mapShow all the related information when the hot spots are clicked.

10	Create a web page with the following.Cascading style sheets Embedded style sheets.Inline style sheets.Use our college information for the web pages.

CSE 03) NAME OF THE LABORATORY: CS 1355 GRAPHICS AND MULTIMEDIA LAB

LIST OF EXPERIMENTAL SETUP –ODD SEMESTER FOR THE ABOVE LAB.

S.No:	Name of the Experiments
1	To implement Bresenham's algorithms for line, circle and ellipse drawing
2	To perform 2D Transformations such as translation, rotation, scaling, reflection and sharing.
3	To implement Cohen-Sutherland 2D clipping and window-view port mapping
4	To perform 3D Transformations such as translation, rotation and scaling.
5	To visualize projections of 3D images
6	To convert between color models.
7	To implement text compression algorithm
8	To implement image compression algorithm
9	To perform animation using any Animation software
10	To perform basic operations on image using any image editing software

(CSE 04) NAME OF THE LABORATORY: CS1307 DATABASE MANAGEMENT SYSTEMS LAB

LIST OF EXPERIMENTAL SETUP –ODD SEMESTER FOR THE ABOVE LAB.

S.No:	Name of the Experiments
1	Data Definition Language (DDL) commands in RDBMS.
2	Data Manipulation Language (DML) and Data Control Language (DCL) commands in RDBMS.
3	High-level language extension with Cursor0s.
4	High level language extension with Triggers
5	Procedures and Functions.
6	Embedded SQL.
7	Database design using E-R model and Normalization.
8	Design and implementation of Payroll Processing System.
9	Design and implementation of Banking System.
10	Design and implementation of Library Information System.

(CSE 05) NAME OF THE LABORATORY: CS 1356 COMPILER DESIGN LAB

LIST OF EXPERIMENTAL SETUP –ODD SEMESTER FOR THE ABOVE LAB.

S.No:	Name of the Experiments
1	Implement a lexical analyzer in "C".

2	Use LEX tool to implement a lexical analyzer.
3	Implement a recursive descent parser for an expression grammar that generates arithmetic expressions with digits, + and *.
4	Use YACC and LEX to implement a parser for the same grammar as given in problem
5	Write semantic rules to the YACC program in problem 5 and implement a calculator that takes an expression with digits, + and * and computes and prints its value
6	Implement the front end of a compiler that generates the three address code for a simple language with: one data type integer, arithmetic operators, relational operators, variable declaration statement, one conditional construct, one iterative construct and assignment statement.
7&8	Implement the back end of the compiler which takes the three address code generated in problems 7 and 8, and produces the 8086 assembly language instructions that can be assembled and run using a 8086 assembler. The target assembly instructions can be simple move, add, sub, jump. Also simple addressing modes are used.

(CSE 06)NAME OF THE LABORATORY: CS 1403 CASE TOOLS LAB

LIST OF EXPERIMENTAL SETUP –ODD SEMESTER FOR THE ABOVE LAB.

S.No:	Name of the Experiments
1	Prepare the following documents for two or three of the experiments listed below and develop the software engineering methodology.
2	Program Analysis and Project Planning.Thorough study of the problem – Identify project scope, Objectives, Infrastructure
3	Software requirement AnalysisDescribe the individual Phases / Modules of the project, Identify deliverables.
4	Data ModelingUse work products – Data dictionary, Use diagrams and activity diagrams, build and test lass diagrams, Sequence diagrams and add interface to class diagrams
5	Software Development and Debugging

(CSE 07) NAME OF THE LABORATORY: CS 1207 SYSTEM SOFTWARE LABORATORY

LIST OF EXPERIMENTAL SETUP –ODD SEMESTER FOR THE ABOVE LAB.

S.No:	**Name of the Experiments**
1	Implement a symbol table with functions to create, insert, modify, search, and display.
2	Implement pass one of a two pass assembler.
3	Implement pass two of a two pass assembler.
4	Implement a single pass assembler.
5	Implement a macro processor.
6	Implement an absolute loader.
7	Implement a relocating loader.

8	Implement pass one of a direct-linking loader.
9	Implement pass two of a direct-linking loader.
10	Implement a simple text editor with features like insertion / deletion of a character, word, and sentence.

(CSE 08) NAME OF THE LABORATORY: CS 1305 NETWORK LAB

LIST OF EXPERIMENTAL SETUP –ODD SEMESTER FOR THE ABOVE LAB.

S.No:	Name of the Experiments
1	Simulation of ARP / RARP
2	Write a program that takes a binary file as input and performs bit stuffing and CRC Computation.
3	Develop an application for transferring files over RS232.
4	Simulation of Sliding-Window protocol.
5	Simulation of BGP / OSPF routing protocol.
6	Develop a Client – Server application for chat.
7	Develop a Client that contacts a given DNS Server to resolve a given host name
8	Write a Client to download a file from a HTTP Server.
9&10	Study of Network Simulators like NS2/Glomosim / OPNET.

(CSE 09) NAME OF THE LABORATORY: CS 1255 VISUAL PROGRAMMING LAB

LIST OF EXPERIMENTAL SETUP –ODD SEMESTER FOR THE ABOVE LAB.

S.No:	Name of the Experiments
1	Writing code for keyboard and mouse events.
2	Dialog Based applications
3	Creating MDI applications
4	Visual C++ Threads
5	Document view Architecture, Serialization
6	Dynamic controls
7	Menu, Accelerator, Tool tip, Tool bar
8	Creating DLLs and using them
9	Data access through ODBC
10	Creating ActiveX control and using it

(CSE 10) NAME OF THE LABORATORY: CS 1254 OPERATING SYSTEMS LAB

LIST OF EXPERIMENTAL SETUP –ODD SEMESTER FOR THE ABOVE LAB.

S.No:	Name of the Experiments
1	Shell programming command syntax write simple functions basic tests
2	Shell programming

	loops patterns expansions substitutions
3	Write programs using the following system calls of UNIX operating system: fork, exec, getpid, exit, wait, close, stat, opendir, readdir
4	Write programs using the I/O system calls of UNIX operating system (open, read, write, etc)
5	Write C programs to simulate UNIX commands like is grep, etc.
6	Given the list of processes, their CPU burst times and arrival times, display/print the Gantt chart for FCFS and SJF. For each of the scheduling policies, compute and print the average waiting time and average turnaround time
7	Given the list of processes, their CPU burst times and arrival times, display/print the Gantt chart for Priority and Round robin. For each of the scheduling policies, compute and print the average waiting time and average turnaround time
8	Implement the Producer – Consumer problem using Semaphores.
9	Implement some memory management schemes – I
10	Implement some memory management schemes – II

LABORATORY FACILITIES

Department: I T

(I T 01)NAME OF THE LAB: IT 1253 SOFTWARE ENGINEERING LAB

LIST OF EXPERIMENTAL SETUP – EVEN SEMESTER FOR THE ABOVE LAB

S.NO	NAME OF THE EXPERIMENTS
1	Library management system
2	Bank Management System
3	Inventory system
4	Software for a Game
5	Text Editor
6	Natural Language Based Grammar Checker
7	Airline Reservation System
8	Online Survey
9	Financial accounting system
10	Graphics Toolkit.

(I T 02)Name of the Lab: CS1207 SYSTEM SOFTWARE LAB

Name of the Lab: CS 1207 SYSTEM SOFTWARE LAB

S.NO	NAME OF THE EXPERIMENTS
1	Implement a symbol table with functions to Create,insert,modify,search,and display
2	Implement pass one of a two pass assembler

3	Implement pass two of a two pass assembler
4	Implement a single pass assembler
5	Implement a macro processor
6	Implement an absolute loader
7	Implement a relocating loader
8	Implement pass one of a direct-linking loader
9	Implement pass two of a direct linking loader
10	Implement a simple text editor with features like insertion/dele character,word,sentence

(I T 03)Name of the Lab: CS 1205 OBJECT ORIENTED PROGRAMMING LAB

LIST OF EXPERIMENTAL SETUP – EVEN SEMESTER FOR THE ABOVE LAB

S.NO	**NAME OF THE EXPERIMENTS**
1	PROGRAMS USING FUNCTIONS
2	SIMPLE CLASSES FOR UNDERSTANDING OBJECTS,MEMBER FUNCTIONS AND CONSTRUCTORS
3	COMPILE TIME POLYMORPHISM
4	RUNTIME POLYMORPHISM
5	FILE HANDLING
6	SIMPLE JAVA APPLICATIONS
7	SIMPLE PACKAGE CREATION
8	INTERFACES
9	THREADING
10	EXCEPTION HANDLING MECHANISM IN JAVA

(I T 04) Name of the Lab: CS1403 CASE TOOLS LAB

LIST OF EXPERIMENTAL SETUP –ODD SEMESTER

S.NO	NAME OF THE EXPERIMENTS
1	Students marks analyzing system
2	Quiz system
3	Online ticket reservation system
4	Payroll system
5	Course registration system
6	Expert systems
7	Atm systems
8	Stock maintenance
9	Real-time scheduler
10	Remote procedure call implementation

(IT 05)Name of the Lab: CS1254 OPERATING SYSTEM LAB(LINUX BASED)

LIST OF EXPERIMENTAL SETUP –EVEN SEMESTER

S.NO	**NAME OF THE EXPERIMENTS**
1	Shell programming-command syntax,write simple functions,basic tests

2	Shell programming-loops,patterns,expansions,substitutions
3	System calls of unix os
4	Using the I/O system calls of unix os
5	Simulate unix commands like ls,grep
6	List of processes –cpu burst times,arrival times,display/print the gantt chart for FCFS and SJF
7	List of processes –cpu burst times,arrival times,display/print the gantt chart for Priority and round robin
8	Implement the producer-consumer problem using semaphores
9	Implement some memory management schemes -I
10	Implement some memory management schemes -II

(IT 06)Name of the Lab: CS1307 DBMS LAB

LIST OF EXPERIMENTAL SETUP –ODD SEMESTER

S.NO	NAME OF THE EXPERIMENTS
1	Data definition language(DDL)
2	Data manipulation Language(DML)
3	High level language Extension with cursors
4	High level language Extension with triggers
5	Procedures and functions
6	Embedded sql
7	Database design using E-R model
8	Design and implementation of payroll processing system
9	Design and implementation of banking system
10	Design and implementation of library information system

(IT 07)Name of the Lab: CS1255 VISUAL PROGRAMMING LAB

LIST OF EXPERIMENTAL SETUP –EVEN SEMESTER

S.NO	NAME OF THE EXPERIMENTS
1	Writing code for keyboard and mouse events
2	Dialog based applications
3	Creating MDI applications
4	Threads
5	Document view architecture,serialization
6	Dynamic controls
7	Menu,accelerator,tooltip,toolbar
8	Creating DLLs and using them
9	Data access through ODBC
10	Creating ActiveX control and using it

(IT 08)Name of the Lab: CS1305 NETWORK LAB

LIST OF EXPERIMENTAL SETUP –EVEN SEMESTER

S.NO	NAME OF THE EXPERIMENTS

1	Simulation of ARP/RARP
2	Write a program that takes a binary file as input and performs bit stuffing and CRC Computation
3	Develop an application for transferring files over RS232
4	Simulation of sliding-window protocol
5	Simulation of BGP /OSPF routing protocol
6	Develop a client-server application for chat
7	Develop a client that contacts a given DNS Server to resolve a given host name
8	Write a client to download a file from a HTTP server
9&10	Study of network simulators like NS2/GLOMOSIM/OPNET

(IT 09)Name of the Lab: IT1403 SOFTWARE COMPONENTS LAB

LIST OF EXPERIMENTAL SETUP –EVEN SEMESTER

S.NO	NAME OF THE EXPERIMENTS
1	COM Component
2	Enterprise java Beans
3	RMI
4	Creation of DLL using VB and deploy it in java
5	Naming services in CORBA
6	DSI,DII IN CORBA
7	Inter ORB in communication
8	Studying J2EE SERVER
9	Simple application using CORBA

(IT 10)Name of the Lab: CS1355 GRAPHICS AND MULTIMEDIA LAB

LIST OF EXPERIMENTAL SETUP –EVEN SEMESTER

S.NO	NAME OF THE EXPERIMENTS
1	To implement bresenham's algorithms for line,circle and ellipse drawing
2	To perform 2Dtransformations such as translation,rotation,scaling reflection and sharing
3	To implement cohen-sutherland 2D Clipping and window-view port mapping
4	To perform 3D transformations such as translation,rotation and scaling
5	To visualize projections of 3D images
6	To convert between color models
7	To implement text compression algorithm
8	To implement image compression algorithm
9	To perform animation using any animation software
10	To perform basic operations on image using any image editing software

LABORATORY FACILITIES

LIST OF MAJOR EQUIPEMENT / FACILITIES

Department: ECE

(ECE 01)Name of the Laboratory: Electronic Devices and Circuits Lab EC1204

S.No.	Description of Equipment	Quantity	Rate(RS.)/unit	TOTALCost(Rs.)
1.	Variable DC Power Supply	12	7200	86400
2.	Variable DC Power Supply	3	3600	10800
3.	CRO	9	22837	205533
4.	Function Generator	12	10500	126000
5.	Decade Resistance Box	8	2250	18000
6.	Decade Capacitance Box	6	2900	17400
7.	Decade Inductance Box	6	2600	15600
Total				463553

LIST OF EXPERIMENTAL SETUP

S.NO	NAME OF THE EXPERIMENTS
1	Diode Forward characteristics.
2	Input and Output characteristics of BJT.
3	Output characteristics of JFET.
4	Fixed Bias amplifier circuits using BJT.
5	BJT Amplifier using voltage divider bias (self bias) with unbypassed emitter resistor.
6	Source follower with Bootstrapped gate resistance.
7	Class B Complementary symmetry power amplifier
8	Differential amplifier using BJT.
9	Power supply Full wave rectifier with simple capacitor filter.
10	Measurement of UJT and SCR Characteristics.

(ECE 02) NAME OF THE LABORATORY: DIGITAL SIGNAL PROCESSING LAB EC1306

S.No.	Description of Equipment	Quantity	Rate(S.)/unit	TOTALCost (Rs.)
1	PCs with Fixed / Floating point DSP Processors (Kit / Add-on Cards)	5	23000	115000
2.	FunctionGenerators (1MHz)	5	4500	22500

3.	CRO (20MHz)	5	22837	114185
Total				251685

LIST OF EXPERIMENTAL SETUP

S.NO	NAME OF THE EXPERIMENTS
1	Study of various addressing modes of DSP using simple programming examples
2	Sampling of input signal and display
3	Implementation of FIR filter
4	Calculation of FFT
5	Generation of Signals
6	Linear and circular convolution of two sequences
7	Sampling and effect of aliasing
8	Design of FIR filters
9	Design of IIR filters
10	Calculation of FFT of a signal

(ECE 03) Name of the Laboratory: Microprocessor and Application Lab EC1303

S.No.	Description of Equipment	Quantity	Rate(RS.)/unit	TOTAL Cost (Rs.)
1	PCs with assembler for 8086 and MCS 51 family	5	30000	150000
2	8085 microprocessor kit	9	6700	60300
3	8086 microprocessor kit	8	6700	53600
4.	8031 microcontroller kit	2	5300	10600
5.	Stepper motor	3	5000	15000
6.	Power supply(+/- 5 V, +/-12V)	20	7200	144000
Total				283500

LIST OF EXPERIMENTAL SETUP

S.NO	NAME OF THE EXPERIMENTS
1	Programs for 8/16 bit Arithmetic operations (Using 8085).
2	Programs for Sorting and Searching (Using 8085, 8086).
3	Programs for String manipulation operations (Using 8086).
4	Programs for Digital clock and Stop watch (Using 8086).
5	Interfacing ADC and DAC.
6	Parallel Communication between two MP Kits using Mode 1 and Mode 2 of 8255.

7	Interfacing and Programming 8279, 8259, and 8253.
8	Serial Communication between two MP Kits using 8251.
9	Interfacing and Programming of Stepper Motor and DC Motor Speed control.
10	Programming using Arithmetic, Logical and Bit Manipulation instructions of 8051microcontroller.
11	Programming and verifying Timer, Interrupts and UART operations in 8031microcontroller.
12	Communication between 8051 Microcontroller kit and PC

(ECE 04) Name of the Laboratory: NETWORKS LAB EC1354

S.No.	Description of Equipment	Quantity	Rate(RS.)/unit	TOTALCost(Rs.)
1.	LAN TRAINER	3	29230	87690
2.	NetworkSimulatorSoftware (Router etc)	15 user	77693	77693
3.	Personal Computers with following specifications P4, 256 MB RAM, 40 GB HDD	15	30000	450000
Total				615383

LIST OF EXPERIMENTAL SETUP

S.NO	NAME OF THE EXPERIMENTS
1	PC to PC Communication Parallel Communication using 8 bit parallel cable Serial communication using RS 232C
2	Ethernet LAN protocol To create scenario and study the performance of CSMA/CD protocol ethrol simulation
3	Token bus and token ring protocols To create scenario and study the performance of token bus and token ring protocols through simulation
4	Wireless LAN protocols To create scenario and study the performance of network with CSMA / CA protocol and compare with CSMA/CD protocols.
5	Implementation and study of stop and wait protocol
6	Implementation and study of Go back-N and selective ret protocols
7	Implementation of distance vector routing algorithm
8	Implementation of Link state routing algorithm
9	Implementation of Data encryption and decryption
10	Transfer of files from PC to PC using Windows / Unix socket processing

(ECE 05) Name of the Laboratory: OPTICAL & MICROWAVE LAB EC1405

S.No.	Description of Equipment	Quantity	Rate(RS.)/unit	TOTAL Cost(Rs.)

1.	Klystron power supply	3	8950	26850
2.	Klystron tube	3	2980	8940
3.	Gunn power supply	1	5965	5965
4.	Gunn oscillator	1	10750	10750
5	PIN modulator	1	4160	4160
6	Isolator	2	3980	7960
7	Attenuator	4	3480	13920
8	Frequency meter	4	7790	31160
9	Slotted section	2	5775	11550
10	Detector mount	4	2760	11040
11	Termination	4	1750	7000
12	Horn antenna	3	1980	5940
13	Magic TEE	1	2000	2000
14	E-Plane TEE	1	2000	2000
15	H-Plane TEE	1	2000	2000
16	Directional coupler	1	3000	3000
17	VSWR meter	2	9980	19960
18	CRO	5	22837	114185
19	Radiation table	1	3000	3000
20	fiber optic trainer Kit	1	4320	4320
			Total	295700

LIST OF EXPERIMENTAL SETUP –.

S.NO	NAME OF THE EXPERIMENTS
1	Numerical aperture determination for fibers and Attenuation Measurement in Fibers.
2	Mode Characteristics of Fibres – SM Fibres.
3	Coupling Fibers to Semi-Conductor Sources – Connectors & Splices.
4	Fiber optic communication links.
5	LED & Photo Diode Characteristics.
6	VSWR Measurements – Determination of terminated impedance
7	Determination of guide wavelength, frequency measurement.
8	Radiation Pattern of Horns, Paraboloids.
9	Microwave Power Measurement.
10	**Characteristics of Gunn diode Oscillator.**

LABORATORY FACILITIES

LIST OF MAJOR EQUIPEMENT / FACILITIES

Department: EEE

(EEE01)Name of the Laboratory: Electrical Workshop Laboratory

S.NO	DESCRIPTION OF ITEMS	QUANTITY	Rate/Unit (Rs)	Total
1	VOLTMETERS (0-300)V MC	5	900/-	4500/-

2	VOLTMETERS (0-300)V MI	5	950/-	4750/-
3	VOLTMETERS (0-600)V MI	5	1310/-	6550/-
4	AMMETERS (0-10)AMPS MI	5	900/-	4500/-
5	AMMETERS(0-20) AMPS MI	5	1100/-	5500/-
6	AMMETERS (0-10) AMPS MC	5	950/-	4750
7	AMMETERS(0-20) AMPS MC	5	1300/-	6500
8	ENERGY METERS	3	2544/-	7600/-
9	WATTMETERS (LPF), (UPF)	4, 4	2067/-	16540/-
10	FLUORSCENT TUBE	5	80/-	400/-
11	LAMP LOAD	2	7500/-	15000/-
12	RESISTIVE LOAD	2	7500/-	15000/-
13	SWITCH BOARDS	4	50/-	200/-
14	WIRES, CUTTER, TESTER, DRILLER, JOINTS, PVC PIPES, INSULATION TAPES	FEW		3500/-
15	AUTO TRANSFORMER(1 PHASE)	2	6250/-	12500/-
16	SPST, DPST, TPST SWITCHES	EACH 5	500/-	2500/-
17	CHOKES, STARTERS	EACH 5	500/-	2500/-
18	STOP WATCH	3	417/-	1250/-
TOTAL		74	TOTAL	114040/-

LIST OF EXPERIMENTAL SETUP

S.NO	LIST OF EXPERIMENTS
1	MEASUREMENT OF POWER USING UPF WATTMETTER
2	MEASUREMENT OF ENERGY CONSUMPTION USING SINGLE PHASE ENERGY METER
3	CALIBRATION OF AMMETER
4	CALIBRATION OF VOLTMETER
5	HOUSE HOLD WIRING
6	FLUORSCENT LAMP WIRING
7	STAIR CASE WIRING

(EEE 02/03)Name of the Laboratory: Electrical Machines Laboratory

S.No	Description of Items	Quantity	Rate/Unit (Rs)	Total
1	DC SHUNT MOTOR	2	28610/-	57220/-
2	DC SERIES MOTOR	2	52100/-	104200/-
3	DC MOTOR COUPLED	2	52100/-	104200/-

	WITH DC GENERATOR			
4	DC COMPOUND MOTOR	2	28610/-	57220/-
5	SINGLE PHASE TRANSFORMER	3	6830/-	20490/-
6	THREE PHASE TRANSFORMER	2	11980/-	23960/-
7	SINGLE PHASE AUTO TRANSFORMER	3	6830/-	20490/-
8	THREE PHASE AUTO TRANSFORMER	3	11980/-	35940/-
9	SINGLE PHASE INDUCTION MOTOR	2	19870/-	39740/-
10	THREE PHASE SLIP RINGINDUCTION MOTOR	2	35933/-	71866/-
11	THREE PHASE SQ. CAGE INDUCTION MOTOR	2	19870/-	39740/-
12	DC SHUNT MOTOR COUPLED WITH AUTO SYNCHRONOUS INDUCTION MOTOR	1	68300/-	68300/-
13	DC SHUNT MOTOR COUPLED WITH THREE PHASE ALTERNATOR	1	63440/-	63440/-
14	RESISTIVE TYPE STARTERS FOR INDUCTION MOTOR	2	5980/-	11960/-
15	SINGLE PHASE RESISTIVE LOAD	2	6830/-	13660/-
16	SINGLE PHASE INDUCTIVE LOAD	2	6830/-	13660/-
17	THREE PHASE RESISTIVE LOAD	2	11420/-	22840/-
18	THREE PHASE INDUCTIVE LOAD	2	11420/-	22840/-
19	DC STARTERS	8	2150/-	17200/-
20	STAR DELTA STARTERS	2	5780/-	11560/-
21	TACHOMETERS DIGITAL TYPE TO MEASURE SPEED UP TO 10000 RPM	5	1870/-	9350/-
22	SYNCHRONOUSING PANEL WITH SYNCHRONOUSCOPE	1	14980/-	14980/-
23	AC DISTRIBUTION PANEL	1	200000/-	200000/-
24	RECTIFIER	1	150000/-	150000/-
25	DC DISTRIBUTION PANEL	1	150000/-	150000/-
TOTAL		56	TOTAL	1344856/-

LIST OF EXPERIMENTAL SETUP

S.NO	NAME OF THE EXPERIMENT
1	LOAD TEST ON DC SHUNT MOTOR
2	SWINBURNES TEST
3	SPEED CONTROL OF DC SHUNT MOTOR
4	LOAD TEST ON SERIES MOTOR
5	HOPKINSONS TEST ON DC MOTOR GENERATOR SET
6	LOAD TEST ON SINGLE PHASE TRANSFORMER
7	LOAD TEST ON THREE PHASE TRANSFORMER
8	SUMPNERS TEST ON TRANSFORMERS
9	SEPERATION OF NO LOAD LOSSES IN SINGLE PHASE TRANSFORMER
10	STUDY OF DC STARTERS
11	REGULATION OF THREE PHASE ALTERNATOR BY MMF, EMF & ZPF METHOD
12	V AND INVERTED V CURVES OF THREE PHASE SYNCHRONOUS MOTOR
13	LOAD TEST ON THREE PHASE INDUCTION MOTOR
14	NO-LOAD AND BLOCKED ROTOR TEST ON THREE PHASE INDUCTION MOTOR
15	SEPERATION OF NO LOAD LOSSES OF THREE PHASE INDUCTION MOTOR
16	LOAD TEST ON SINGLE PHASE INDUCTION MOTOR
17	NO-LOAD AND BLOCKED ROTOR TEST ON SINGLE PHASE INDUCTION MOTOR
18	STUDY AC STARTERS

(EEE 04)Name of the Laboratory: Control Systems Laboratory

S.No	Description of Items	Quantity	Rate/unit (Rs)	Total
1	DC SERVOMOTOR	1	16380/-	16380/-
2	AC SERVOMOTOR	1	13870/-	13870/-
3	ANALOG SIMULATION OF TYPE 0 AND TYPE 1 SYSTEM MODULE	EACH 1	4350/-	4350/-
4	REGULATED POWER SUPPLY	4	562/-	2250/-
5	CRO	4	37500/-	150000/-
6	TACHOMETER	4	1125/-	4500/-
7	MULTIMETER	4	100/-	400/-
8	SYNCHRONOUS(TRANSMETER AND RECIEVER)	1 SET	35550/-	35550/-
9	MODULE OF P, PI,PID CONTROLLERS	EACH 1	19550/-	19550/-
10	LEAD/LAG COMPENSATOR MODULE	EACH 1	22450/-	22450/-
11	STOP WATCH	4	250/-	1000/-
12	RIGGEDUP MODULES OF A LINEAR SYSTEMS	1	3350/-	3350/-
13	VARIABLE R, L,C BOXES	EACH 2	575/-	1150/-
14	PROCESS CONTROL TRAINER	1	2250/-	2250/-
15	SYSTEM WITH MATLAB,	5 USER	500000/-	2500000/-

	MATHCAD, PSPICE SOFTWARES	LICENCE		
16	TEMPERATURE CONTROLLER WITH PC CONNECTED SOFTWARE	2	75000/-	1500000/-
17	MODULE OF STUDY UNIT OF DC SERVOMOTOR	1	52150/-	52150/-
18	RHEOSTAT RANGE 270 OHM, 5 AMPS	3	883/-	2650/-
19	RHEOSTAT RANGE 60 OHMS 2 AMPS	3	550/-	1650/-
20	AC CONTROLLER WITH DATA CHORD WITH PC SOFTWARE	2 UNITS, 2 USER LICENSE, 6 CHORDS	3000/- 62500/- 7542/-	6000/- , 125000/-, 45250/-
21	SQUARE WAVE GENERATOR	4	13125/-	52500/-
22	DSO	2	32625/-	65250/-
	TOTAL	58	TOTAL	5955976/-

LIST OF EXPERIMENTAL SETUP

S.NO	NAME OF THE EXPERIMENT
1	DETERMINATION OF TRANSFER FUNCTION OF A DC SERVO MOTOR
2	DETERMINATION OF TRANSFER FUNCTION OF A AC SERVO MOTOR
3	ANALOG SIMULATION OF TYPE 0 AND TYPE 1 SYSTEMS
4	DIGITAL SIMULATION OF LINEAR SYSTEM
5	DIGITAL SIMULATION OF NON LINEAR SYSTEM
6	DESIGN AN IMPLEMENTATION OF COMPENSATORS
7	TRANSFER FUNCTION OF ARMATURE CONTROL
8	TRANSFER FUNCTION OF FIELD CONTROL OF DC MOTOR
9	TIME RESPONSE ANALYSIS
10	DESIGN OF P,PI,PID CONTROLLERS
11	STABILITY ANALYSIS OF LINEAR SYSTEM- BODE PLOT,NYQUIST PLOT,ROOT LOCUS
12	CLOSED LOOP CONTROL SYSTEM- TEMPERATURE CONTROL SYSTEM
13	STUDY OF SYNCHROS

(EEE05)Name of the Laboratory: Power Electronics Laboratory

S.NO	DESCRIPTION OF ITEMS	QUANTITY	Rate/Unit (Rs)	Total
1	DEVICE CHARACTERISTICS (FOR SCR, MOSFET ,IGBT &TRIAC) KIT WITH BUILTIN POWER SUPPLY AND METERS	1	10500/-	10500/-
2	SCR FIRING CIRCUIT	2	7560/-	15120/-

	MODULE			
3	SINGLE PHASE SCR BASED HALF CONTROLLED CONVERTER AND FULLY CONTROLLED CONVERTER ALONG WITH BUILT-IN / SEPARATE/FIRING CIRCUIT/MODULE AND METER	EACH 2		26750/-
4	MOSFET BASED STEPUP AND STEPDOWN CHOPPERS	1	7250/-	7250/-
5	IGBT BASED SINGLE PHASE PWM INVERTER MODULE	2	8325/-	16650/-
6	IGBT BASED THREE PHASE PWM INVERTER MODULE	2	9825/-	19650/-
7	IGBT BASED HIGH SWITCHING FREQUENCY CHOPPER MODULE WITH BUILT-IN CONTROLLER	1	8850/-	8850/-
8	RESONANT DC-DC CONVERTER MODULE WITH BUILT IN POWER SUPPLY AND CONTROLLER	1	16350/-	16350/-
9	SCR AND TRIAC BASED SINGLE PHASE AC PHASE CONTROLLER ALONG WITH LAMP AND RESISTIVELOAD	2	26733/-	26733/-
10	SCR BASED V/I COMMUTED CHOPPER MODULE WITH RELEVANT FIRING MODULE (SEPARATE OR BUILT IN)	2	16280/-	32560/-
11	DUAL REGULATED DC POWER SUPPLY WITH COMMON GROUND	4	4053/-	16210/-
12	CATHODE RAY OSCILLOSCOPE	4	14105/-	56420/-
13	ISOLATION TRANSFORMER	3	7447/-	22340/-
14	SINGLE PHASE AUTO TRANSFORMER	3	6830/-	20490/-
	TOTAL	30	TOTAL	295873/-

LIST OF EXPERIMENTAL SETUP

S.NO	LIST OF EXPERIMENTS
1	THE FORWARED CHARACTERISTICS OF SCR
2	CHARACTERISTICS OF IGBT AND MOSFETS
3	TRANSIENT CHARACTERISTICS OF SCR AND MOSFETS

4	AC TO DC FULLY CONTROL CONVERTER
5	AC TO DC HALF CONTROL CONVERTER
6	STEPDOWN /STEPUP MOSFET BASED CHOPPER
7	IGBT BASED SINGLE PHASE PWM INVERTER
8	IGBT BASED THREE PHASE PWM INVERTER
9	CHARACTERISTICS OF TRIAC
10	RESONANT DC-DC CONVERTER

(EEE06)Name of the Laboratory: Measurement and Instrumentation Lab

S.NO	DESCRIPTION OF ITEMS	QUANTITY	RATE/UNIT (Rs)	TOTAL
1	LVDT KIT	1	7500/-	7500/-
2	MULTIMETER	5	400/-	2000/-
3	BOURDON PRESSURE TRANSDUCER KIT	1	15550/-	15550/-
4	FOOT PUMP	1	13450/-	13450/-
5	VOLTMETER	4	400/-	1600/-
6	MAXWELLS INDUCTANCE CAPACITANCE BRIDGE KIT	1	13240/-	13420/-
7	UNKNOWN INDUCTANCE	1	1450/-	1450/
8	SCHERING BRIDGE KIT	1	13450/-	13450/-
9	UNKNOWN CAPACITANCE	1	1450/-	1450/-
10	WHEATSTONE BRIDGE KIT	1	2650/-	2650/-
11	UNKNOWN RESISTANCE	1	1450/-	1450/-
12	KELVIN DOUBLE BRIDGE KIT	1	12350/-	12350/-
13	OPERATIONAL AMPLIFIER	1	4560/-	4560/-
14	RESISTOR	3	2/-	6/-
15	RPS	4	3200/-	12800/-
16	IC 741	2	12/-	24/-
17	DC TRAINER KIT	2	8250/-	16500/-
18	CRO	2	22500/-	45000/-
19	RESISTANCE	1	10	10/-
20	CAPACITANCE	1	25/-	25/-
21	ENERGY METER	1	3250/-	3250/-
22	WATTMETER	2	1750/-	3500/-
23	STOP WATCH	1	200/-	200/-
24	AMMETER (MI, MC)	EACH 2	1500/	3000/-

25	VOLTMETER(MI,MC)	EACH 2	1500/-	1500/-
26	CURRENT TRANSFORMER	1	6380/-	6380/-
27	LAMP LOAD(60 WATTS)	1	35/-	35/-
28	THREE PHASE AUTO TRANSFORMER	1	11380/-	11380/-
29	MAXWELL BRIDGE SETUP	1	5500/-	5500/-
30	GALVANOMETER	1	4500/-	4500/-
TOTAL		48	TOTAL	204490/

LIST OF EXPERIMENTAL SETUP

S.NO	LIST OF EXPERIMENTS
1	STUDY OF OPERATION OF LVDT
	STUDY OF OPERATION OF BOURDON TUBE
3	MEASUREMENT TO FIND UNKNOWN INDUCTANCE AND Q FACTOR OF A COIL
4	MEASUREMENT OF UNKNOWN CAPACITANCE USING SHEARING BRIDGE
5	MEASUREMENT OF MEDIUM RESISTANCE USING WHEATSTONE BRIDGE
6	MEASUREMENT OF LOW RESISTANCE USING KELVINS DOUBLE BRIDGE
7	STUDY OF OPERATION OF ISTRUMENTATION AMPLIFIER
8	DESIGN AND TEST A 4 BIT A/D CONVERTER
9	DESIGN AND TEST A 4 BIT D/A CONVERTER
10	STUDY OF TRANSIENT RESPONSE OF A GIVEN SYSTEM
11	STUDY THE WORKING OF CURRENT TRANSFORMER
12	CALIBRATION OF SINGLE PHASE ENERGY METER AT UNITY AND OTHER POWER FACTORS
13	MEASUREMENT OF THREE PHASE POWER AND POWER FACTOR
14	MEASUREMENT OF IRON LOSS (MAXWELL BRIDGE)

COMPUTING FACILITIES:

Number and Configuration of Systems	450 systems
Total number of systems connected by LAN	180 systems
Total number of systems connected to WAN	60 systems
Internet bandwidth	(i) 512 KBPS – 6 connections (ii) 256 Kbps – 1 connection
Major software packages available	14 packages

Special purpose facilities available	Broadband Internet facility

LIST OF SOFTWARE

OPERATING SYSTEMS:

1. Windows 2003 small business server -Legal Software
2. Windows XP Professional -Legal Software
3. Linux -Freeware

Application Software:

1. Microsoft office XP Professional -Legal Software
2. Microsoft office Professional 2003 -Legal Software
3. Microsoft Visual Studio. Net -Legal Software
4. Microsoft MSDN Library -Legal Software
5. Macromedia STUDIOMX -Legal Software
6. Microsoft SQL SERVER -Legal Software
7. Tomcat Server -Freeware

System Software:

1. Turbo C++ suite -Legal Software
2. Java Development Kit 1.5 -Freeware
3. JSDK -Freeware
4. ORACLE 10G -Legal Software

WORKSHOP:

A huge workshop has been constructed to house almost all the laboratories. However the first year workshop now houses in an extent of 1000 sq.m and has all the equipment/instruments Workshop: The Workshop complex houses carpentry, fitting, smithy, foundry and welding shops with proper ventilation and

illumination. Each shop is provided with proper workbenches with all the required tools modern equipments have been added to each shop, particularly the foundry shop. Foundry shop is facilitated with various types of sand testing equipments.

First year students of all branches are trained here in common workshop practices.

LIST OF FACILITIES AVAILABLE.

Games and Sports Facilities A very large play ground with modern amenities available. We have facilities for Volleyball courts, One Badminton courts ,One Football ground, One Hockey ground, Two pitches for Cricket, Athletic tracks. Athletic field, Table tennis room, Basket ball court, Lawn tennis court, Gymnasium, Chess, and Carrom	
Extra Curricular Activities: NSS,YRC, Rural Development Cell available	
Soft Skill Development Facilities: Available through Placement and Training Center. Specialized training in learning and communication skills development towards students' effective reading, comprehension, listening, dictation and note taking, memorizing, innovative thinking skills is arranged with cooperation of subject experts from noted Institutions.	
Number of Classrooms and size of each :	25 (Each class room 76 Sqmeter
Number of Tutorial rooms and size of each :	10 (Each Room 36 Sqmeter)
Number of Computer Centres with capacity of each:	Four Each accommodating 100 students
Drawing Hall (*)	

Instructional Area for the existing programme(s)

Particulars	Number of rooms		Carpet area of each room	
	Requirement as per norms	Available in the institution	Requirement as per norms	Available in the Institution (Sq.M)
Class Rooms		**34**		**1650 each 66 Sq.m**
Tutorial Hall		**12**		**330 each 33Sq.m**
Drawing Hall (*)		**2**		**520 Sq.m**
Computer Centre		**2**		**1000**
Library		**1**		**800**
Laboratories & workshops		**1**		**800**
Physics Lab		**1**		**224**
Chemistry		**1**		**249**

Electrical Lab		**2**		**448**
Computer Lab		**1**		**243**
Communication Lab		**1**		**175**
Circuit Lab		**1**		**175**
Microprocessor Lab		**1**		**175**
Microwave and optical Comn Lab		**1**		**175**
Machine Lab		**1**		**200**
FM Lab		**1**		**200**
Work Shop		**1**		**1198**
Fashion Technology Lab		**1**		**338**
Total				**8100**

SKEC ALUMINI

The Alumni Association is for rendering help/assistance to all the passed-out students required. A data Bank is maintained to store all information about such students. Alumni meets are organized periodically at Chennai with the prime objective of widening the Alumni base of our College. The students' sentiments for professional work culture are recorded and acted upon. They will give proposals for taking action to widen the Alumni base.

CURRICULA AND SYLLABI FOR EACH OF THE PROGRAMS AS APPROVED BY THE UNIVERSITY.

The syllabus is strictly as prescribed by affiliating University. On joining, the students are furnished with the detailed syllabus and curriculum details: The same is available at the Anna University Web Site: www.annauniv.edu with link with "1st to 8th Semester".

Academic Calendar of the University:

The academic Calendar furnished by the Anna University is individually given to the **Students:**

Academic Time Table:

Semester wise Time Table is being formulated and circulated to the students

Teaching Load of each Faculty:

This is as per the AICTE Norms of two subjects and one Laboratory per teacher per semester- A minimum of 4hours of teaching and 6 hours of Laboratory practical.

Internal Continuous Evaluation System in place: ***Tests, assignments,*** *assessment tests and Model Examinations & seminars are conducted vigorously.*

Students' assessment of Faculty, System in place: *This is being undertaken by the students two times in a semester. Class Committee Meeting comprising Student representatives and the Class Teachers and Quality circle Meetings & end semester evaluation*

TRAINING AND PLACEMENT ACTIVITIES AND STATISTICS

The College has strength in its Placement Centre which is headed by **Prof.S.VIJAY** who serves the Centre with dedication and he enjoys the confidence of the students by virtue of his swift movement with companies. The centre arranges periodically Placement Programs, aptitude training and in-plant training. On 05.01.2008 the centre arranged a mega placement drive by Mphasis, An EDS Company for recruitment of B.E and other graduates. More than 1100 students participated. In the coming days a number of companies have agreed to visit our College. The Centre, Placement and Training activities are assisted by one faculty from each department. The Centre interacts effectively with industries in and around Chennai. The Centre organizes campus recruitment, in-plant training on a regular basis.

The Centre also arranges training of teachers for their professional development and career improvement which is a significant aspect. The Centre is for ensuring:

- A good liaison with industry,
- Watch on the job requirements in the industries.
- Campus recruitment and in-plant training.
- Contact with information experts in respective fields from industries.
- Special lectures for the benefit of the students and as well the staffs.
- Training for teacher's and staff.
- A good data bank of the alumni
- Industrial training and field/industrial visits for the students.
- Assistance to students in getting apprenticeship training.

The following are the statistics of our students' placement records by the Centre:

2000-2004 Batch

Sl.No.	Names of the students	Branch of study	Company where placed
1	G.Shanthi	CSE	TCS
2	R.Anandhi	CSE	CTS
3	Fredrick Moses	CSE	POLARIS
4	S.Priya	ECE	WIPRO
5	P.Sumathi	CSE	CTS
6	Zinap Fathima	ECE	CTS
7	S.Divakar	IT	Millinum Software
8	G.Nithya	CSE	CTS
9	T.Jayanthi	IT	INFOSYS
10	Jansi Merlin	CSE	CTS
11	M.Dayanithi	CSE	HCL
12	R.Revathi	IT	CTS
13	M.Vishnu Prasad	ECE	HUTCH
14	P.Dinesh Kumar	ECE	HCL
15	A.Mythreyi	CSE	HUTCH
16	Ponsaravanan	CSE	HCL
17	Sri Lakshmi	ECE	WIPRO
18	K.Balaji	ECE	VLSI
19	J.Venkatesh	IT	CTS
20	S.Sherin Farhana	CSE	CTS

2001-2005 Batch

1	**Raghuveer**	**CSE**	**HCL**
2	**Shyamala**	**CSE**	**CTS**

3	Devendra Kumar	CSE	TCS
4	Susmitha	CSE	INFOSYS
5	Paul Antony	CSE	POLARIS
6	Saranya	CSE	Hutch
7	Dhanalakshmi	CSE	WIPRO
8	Umashankar	CSE	CTS
9	Vinod Kumar	CSE	INFOSYS
10	Sundaraj	CSE	VIRTUSA
11	BVN Rani	CSE	Millinum Software
12	Jansi Sangeetha	CSE	Scope International
13	M.Aswini	CSE	ICICI
14	L.Parthasarathy	CSE	INFOSYS
15	G.Gowri	CSE	HCL
16	P.Amarasree	CSE	CTS

2002-2006 Batch

1.	G.Archana	ECE	POLARIS
2.	S.Vijayakumar	ECE	Hutch
3.	K.Jayaraman	ECE	IBM
4.	K.Sriram	ECE	CTS
5.	S.R.Thamotharan	ECE	TCS
6.	S.Geetha	ECE	ACCENTURE
7.	R.Kameshwaran	ECE	ICICI
8.	S.Ibrahim	ECE	HCL
9.	P.Venkata Siva Reddy	ECE	TCS
10.	S.Leelavathy	ECE	INFOSYS
11.	Dhayanidhi	ECE	Sutherland
12.	K.Uma	ECE	AllSEC

13.	G.Shobana	ECE	WIPRO
14.	A.Praveen Kumar	ECE	HCL
15.	N.Suganya	ECE	INFOSYS
16.	T.Vidhya	ECE	TCS
17.	M.Arif Ibrahim	ECE	Satyam

2003-2007 Batch

Sl.No.	Names of the students	Branch of study	Company where placed
1	Yugaselvi	ECE	Sutherland
2	Kalaiarasi	ECE	Sutherland

2004-2008 Batch

Sl.No.	Names of the students	Branch of study	Company where placed
1	N.Gopinath	CSE	EDS
2	N.Srinivasan	CSE	EDS
3	K.Deepika	ECE	EDS
4	D.Hemavathi	ECE	EDS
5	V.Lavanya	ECE	EDS
6	G.Ramyashree	ECE	Network Solutions
7	Lochan Jothi	ECE	Cybernet
8	M.C.Lavanya	ECE	Cybernet
9	M.Aruna	CSE	Cybernet

For each PG Programme give the following:

- **Special Purpose Software., - All design tools, Special Purpose VLSI Design tools**
- **Academic Calendar and frame work**
    - **Same as UG with 90 working days (As Per Anna University)**
- **Research focus :**
    - **Design and development of Embedded Systems- Industry Oriented**
- **Industry Linkage:**
    - **Nokia, MM Forgings, Applo Types and Flextronics**
- **Publications (if any) out of research in last three years out of masters projects:**
- **Admission Procedure:**
    - **Same as Anna University norms**
- **Fee Structure:**
    - **As per Govt norms**
- **Hostel Facilities:**
    - **Available**
- **Contact Address of Co-coordinator of the PG Programme:**
    - **Prof.N.SIVANESAN**

Note: Suppression and or misrepresentation of information would attract panel Action

Sd. R.Vivekanandhan
Chairman,
Srikrishna Educational Trust running the Srikrishna Engineering College